


FORM PTO-1449		Atty. Docket No. ATM-276	Serial No. 10/810,033
LIST OF PRIOR ART CITED BY APPLICANT		Applicants: Jean-Michel Daga et al.	
		Filing Date: March 26, 2004	Group: 2816


## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Name	Class	Sub Class	Filing Date
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					

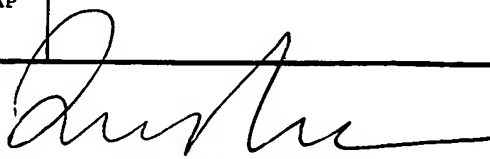
## FOREIGN PATENT DOCUMENTS

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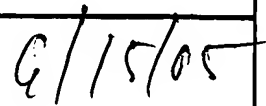
## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	Article entitled "A New High Efficiency CMOS Voltage Doubler" by Pierre Favrat et al., Swiss Federal Institute of Technology Electronics Labs EL-LEG, Ecublens, CH-1015 Lausanne, Switzerland and Mead Microelectronics Engineering Applications and Design SA, Venoge 7, CH-1025 St. Sulpice, Switzerland, IEEE 1997 Custom Integrated Circuits Conference, pages 259-262 (0-7803-3669-0).
	AP	

EXAMINER:

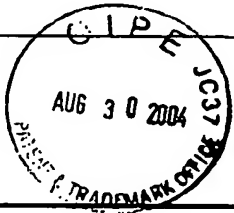


DATE CONSIDERED:



\*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

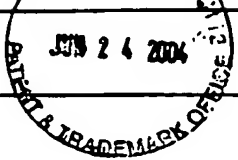

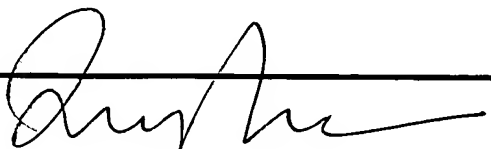
AO	S-C Lee et al., A Low-Ripple Switched-Capacitor DC-DC up Converter for Low-Voltage Applications, IEICE Transactions on Elect., Instit. Electronics Information and Comm. Eng., August 2001, Vol. E84-C, No. 8, pages 1100-1103, XP001107649, ISSN 0916-8524.
AP	K-S Min et al., CMOS Charge Pumps Using Cross-Coupled charge Transfer Switches with Improved Voltage Pumping Gain and Low Gate-Oxide Stress for Low-Voltage Memory Circuits, IEICE Transactions on Electron., Instit. Electronics Information and Comm. Eng., January 2002, Vol. E85-C, No. 1, January 2002, XP001116786, ISSN 0916-8524.

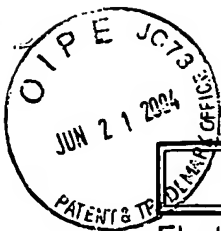
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6/15/03

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AK							
AL							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AM	Hongchin Lin et al., "New Four-Phase Generation Circuits for Low-Voltage Charge Pumps", IEEE, 2001, pp. 504-507.					
	AN						
	AO						
EXAMINER: 				DATE CONSIDERED: 6/15/03			
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## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of  
Invention

HIGH EFFICIENCY, LOW COST, CHARGE PUMP CIRCUIT

Application Number: 10/810033



Confirmation Number: 8427

First Named Applicant: Jean-Michel Daga

Attorney Docket Number: ATM-276

Art Unit: 2816

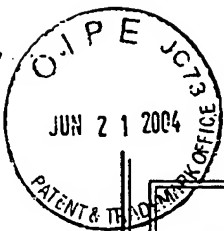
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or 6166585 or 6353356 or 6429723 or 6437637  
or 6448841 or 6466079 or 6515535 ),pn.

### US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	4581546	1986-04-08	Allan	A1	307	297
<input type="checkbox"/>	2	5196996	1993-03-23	Oh	A1	363	60
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<input type="checkbox"/>	8	6163487	2000-12-19	Ghilardelli	A1	365	189.09
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<input type="checkbox"/>	10	6353356	2002-03-05	Liu	A1	327	536
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<input type="checkbox"/>	13	6448841	2002-09-10	Milazzo	A1	327	536
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Signature



Examiner Name	Date
<i>[Signature]</i>	6/15/05